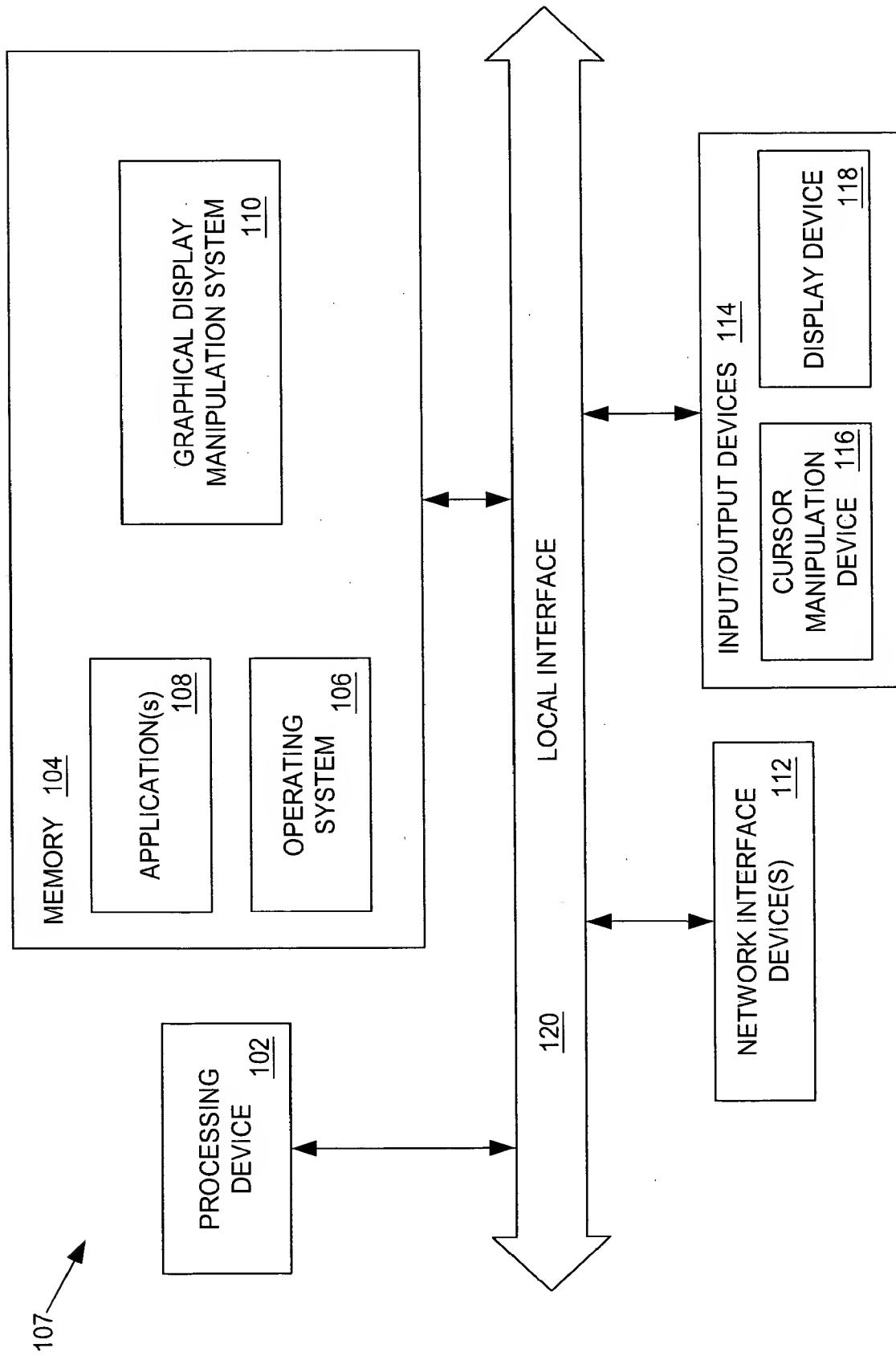
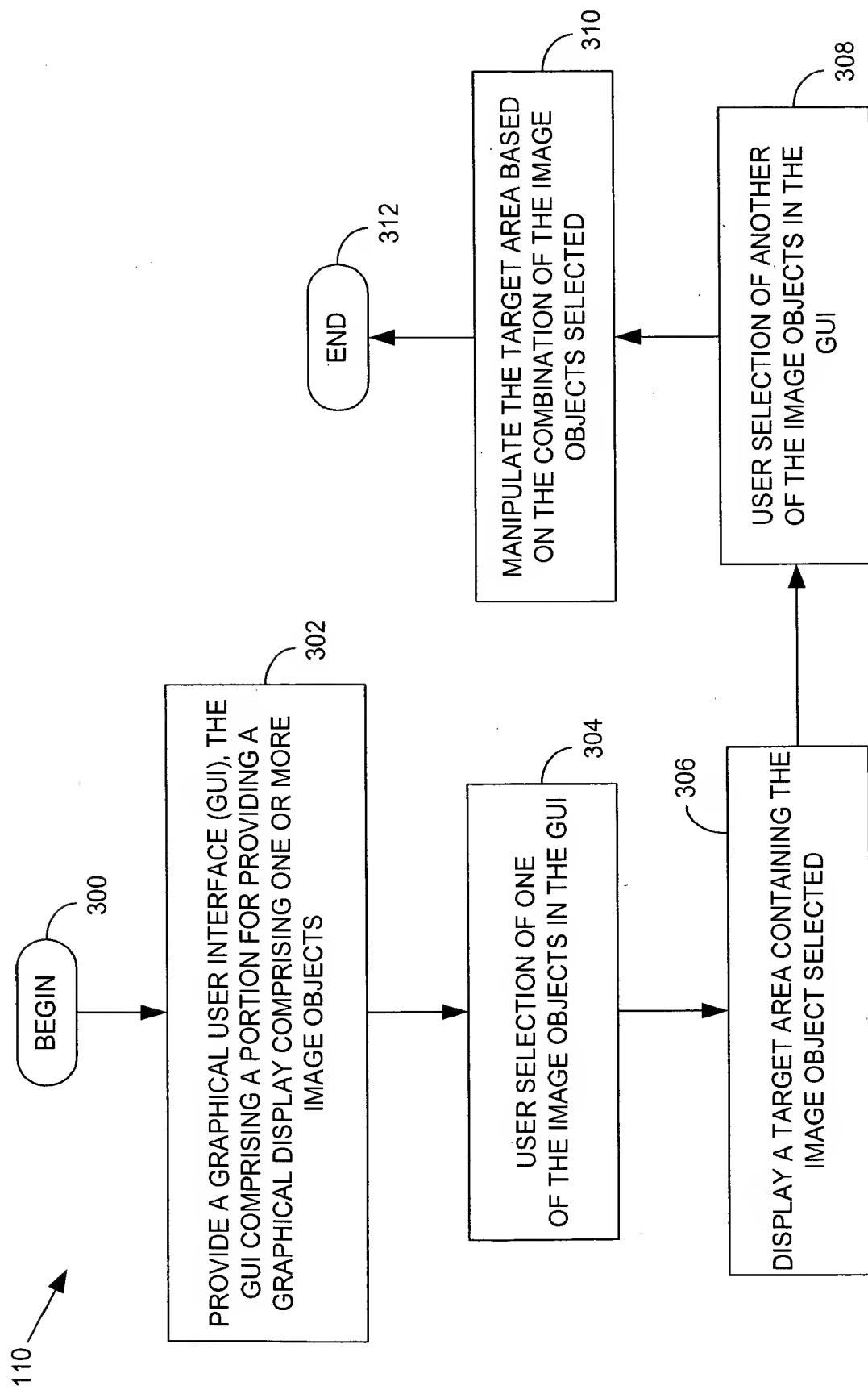


**FIG. 1**



**FIG. 2**



**FIG. 3**

400

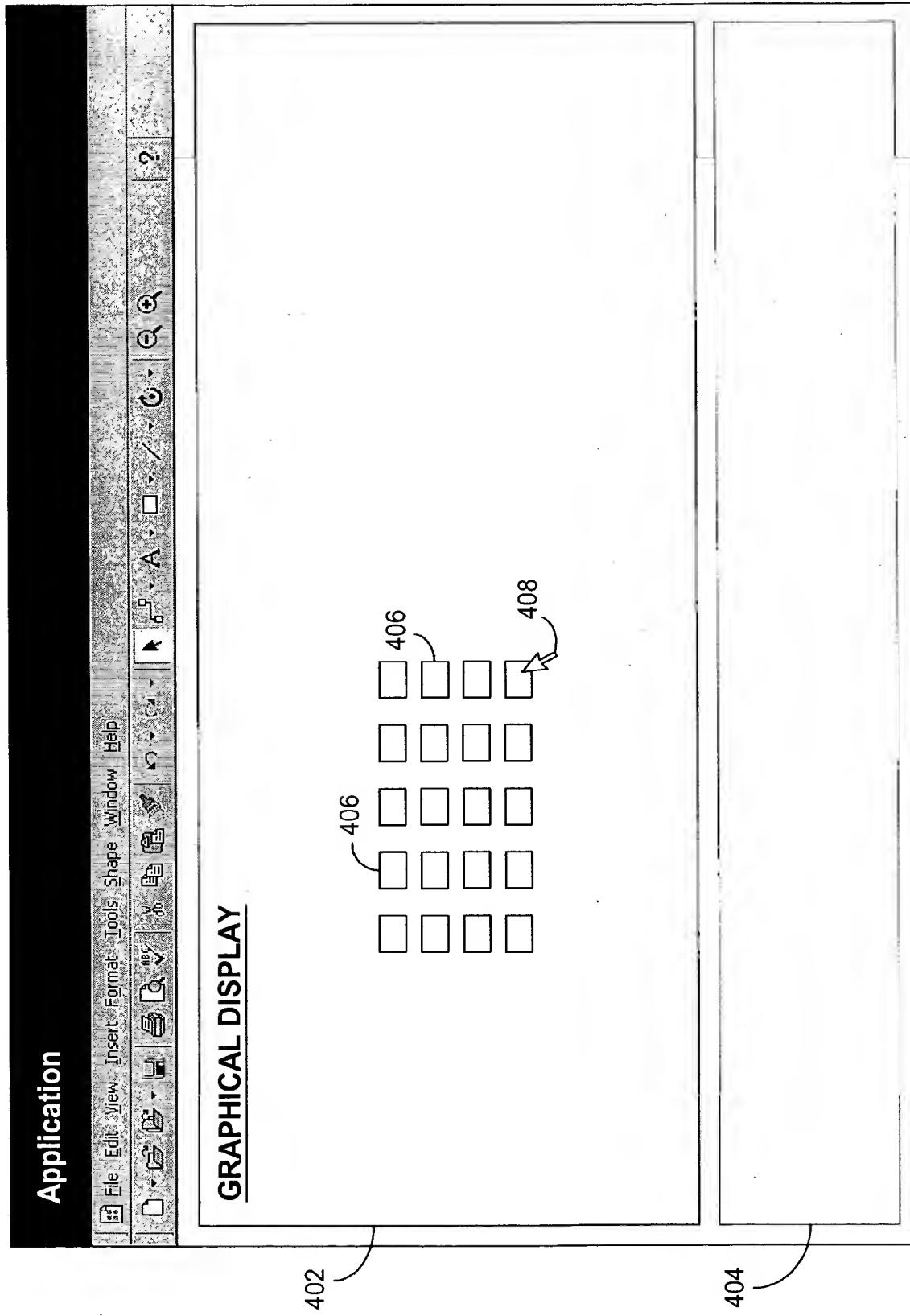


FIG. 4

400

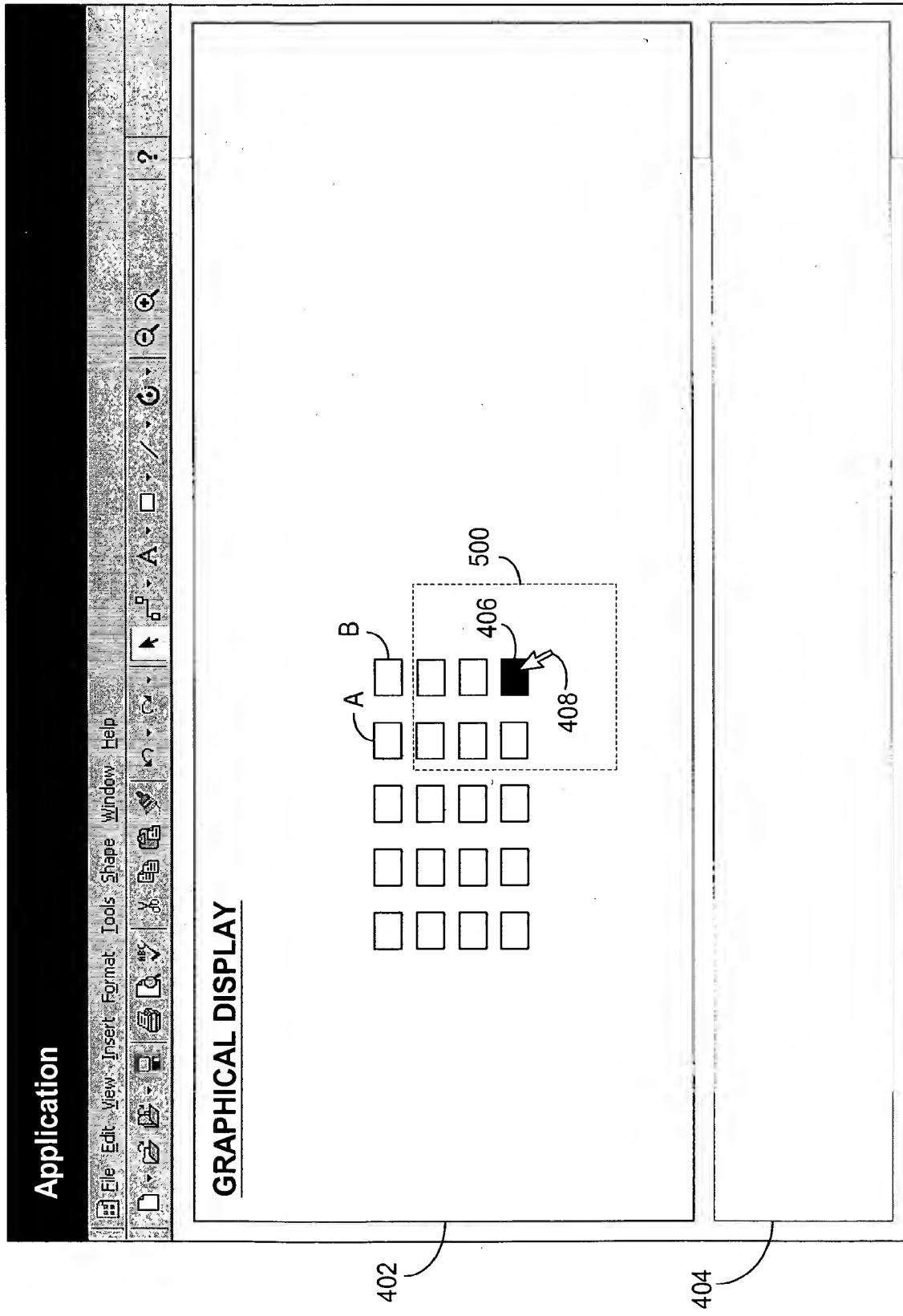
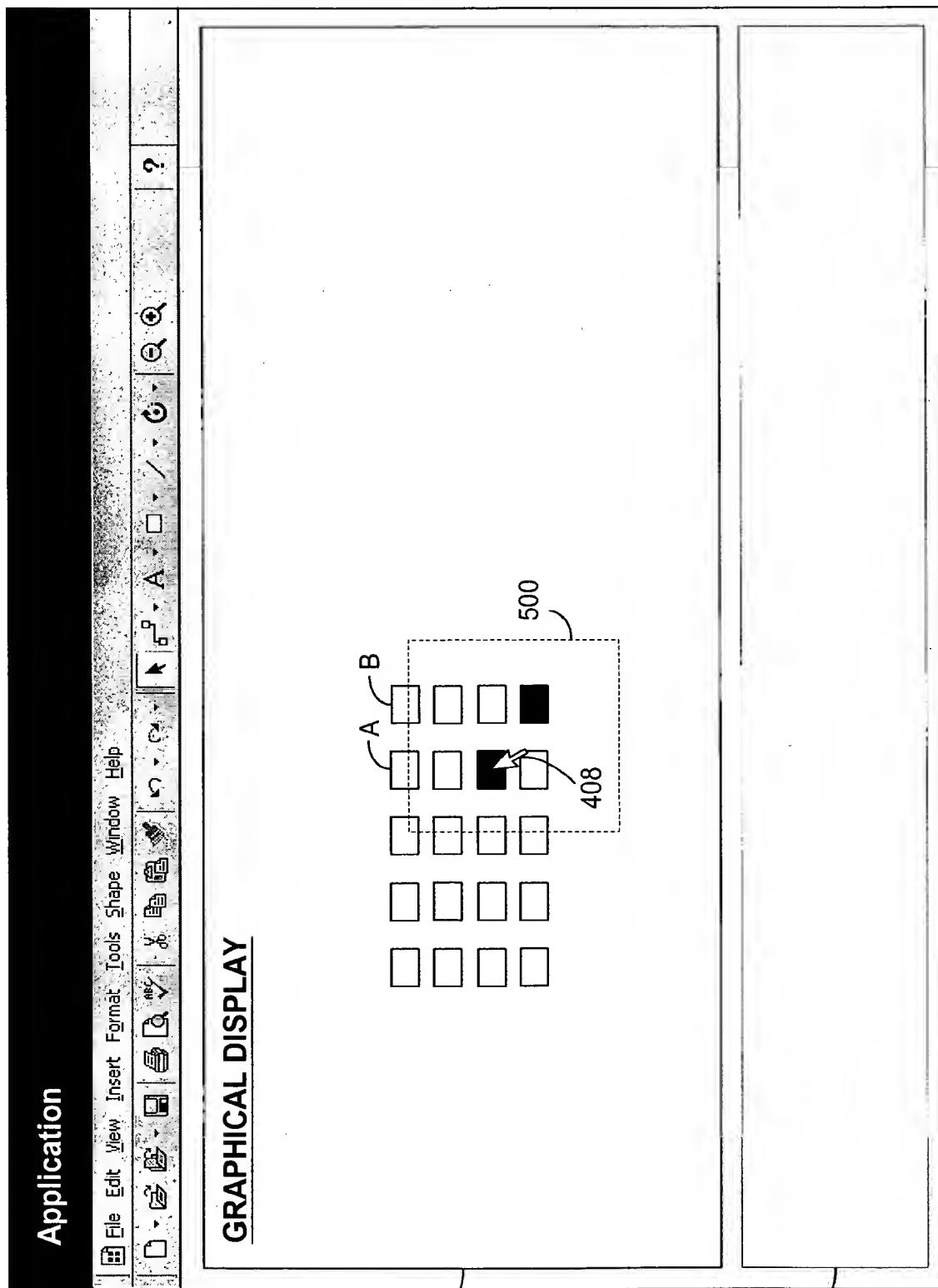


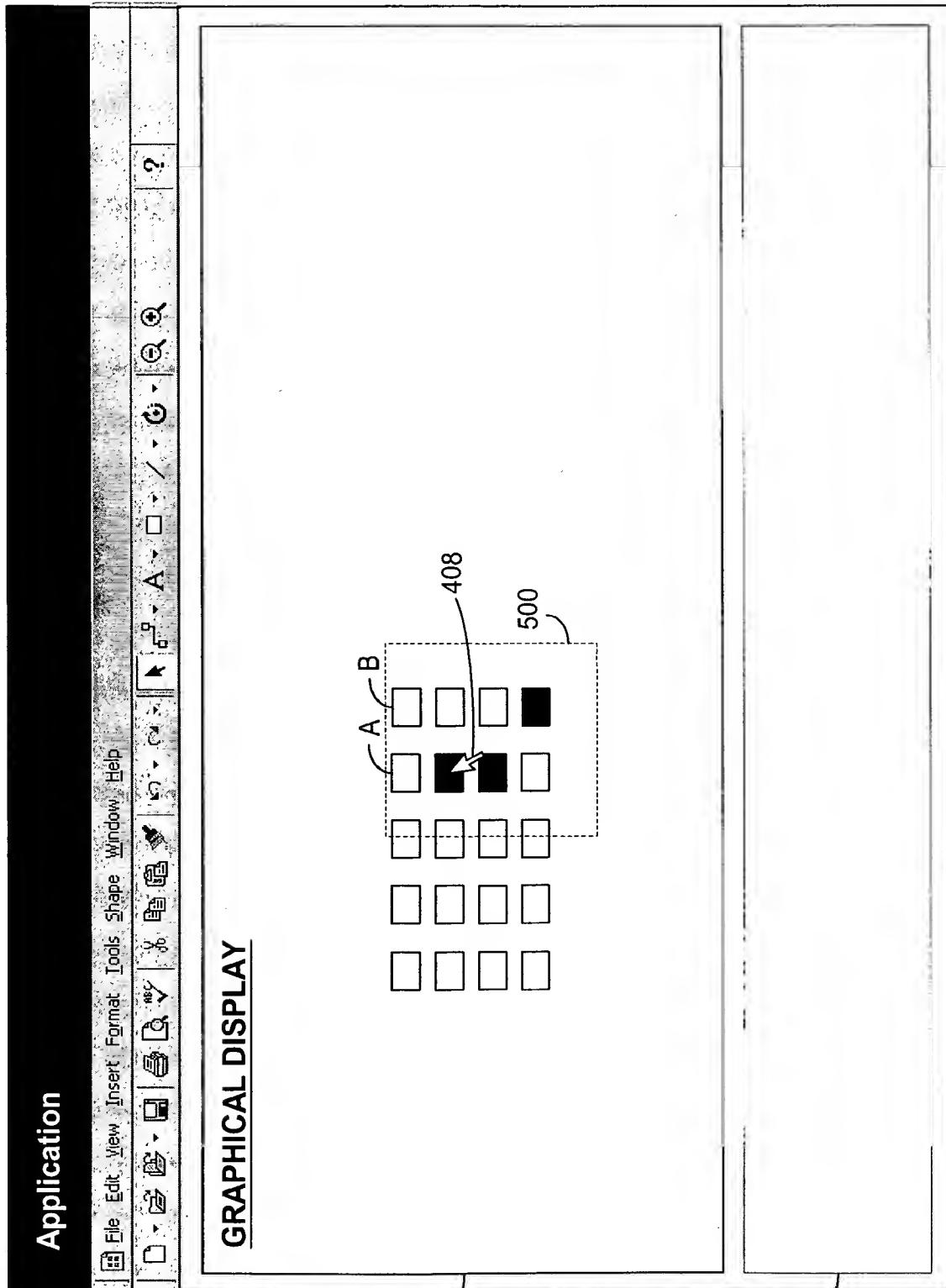
FIG. 5

400



**FIG. 6**

400

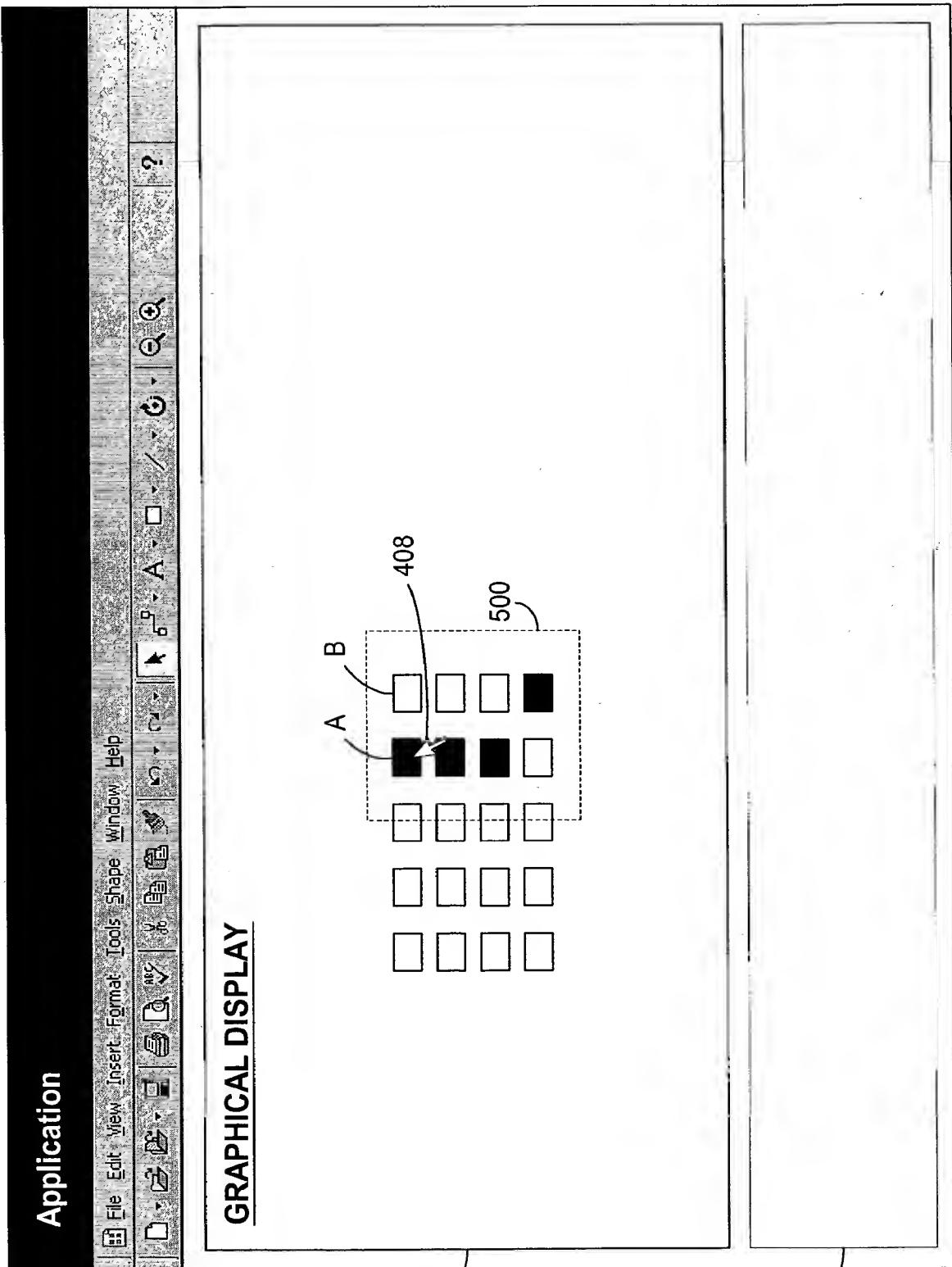


402

404

FIG. 7

400

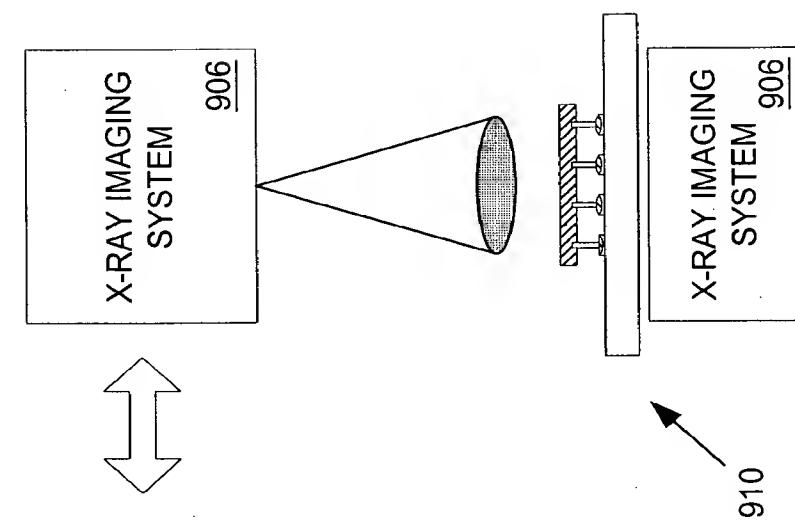
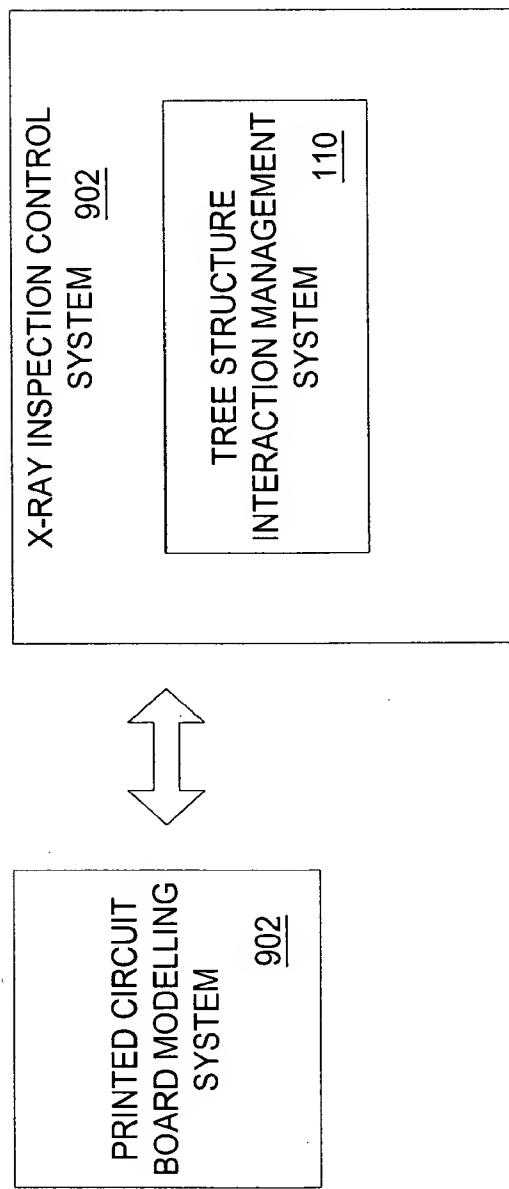


**FIG. 8**

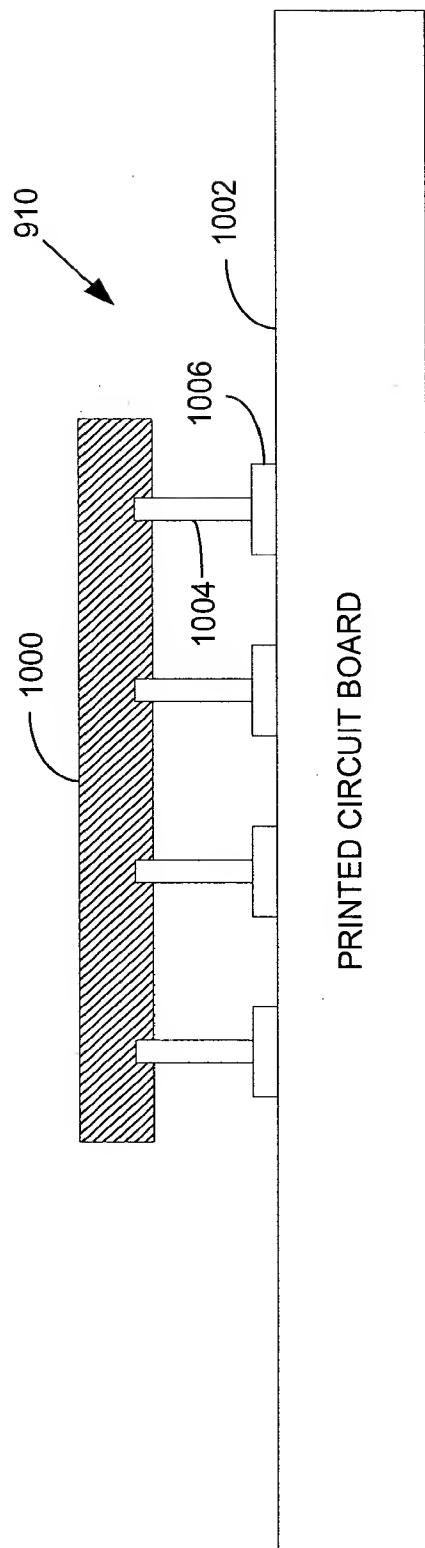
404

402

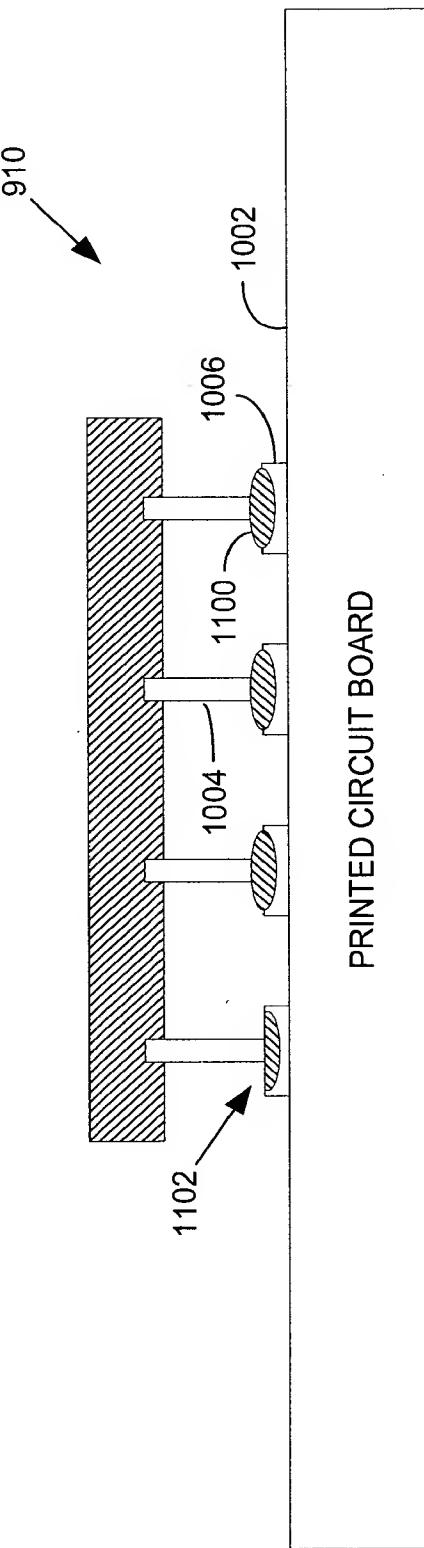
PRINTED CIRCUIT BOARD MODELLING SYSTEM



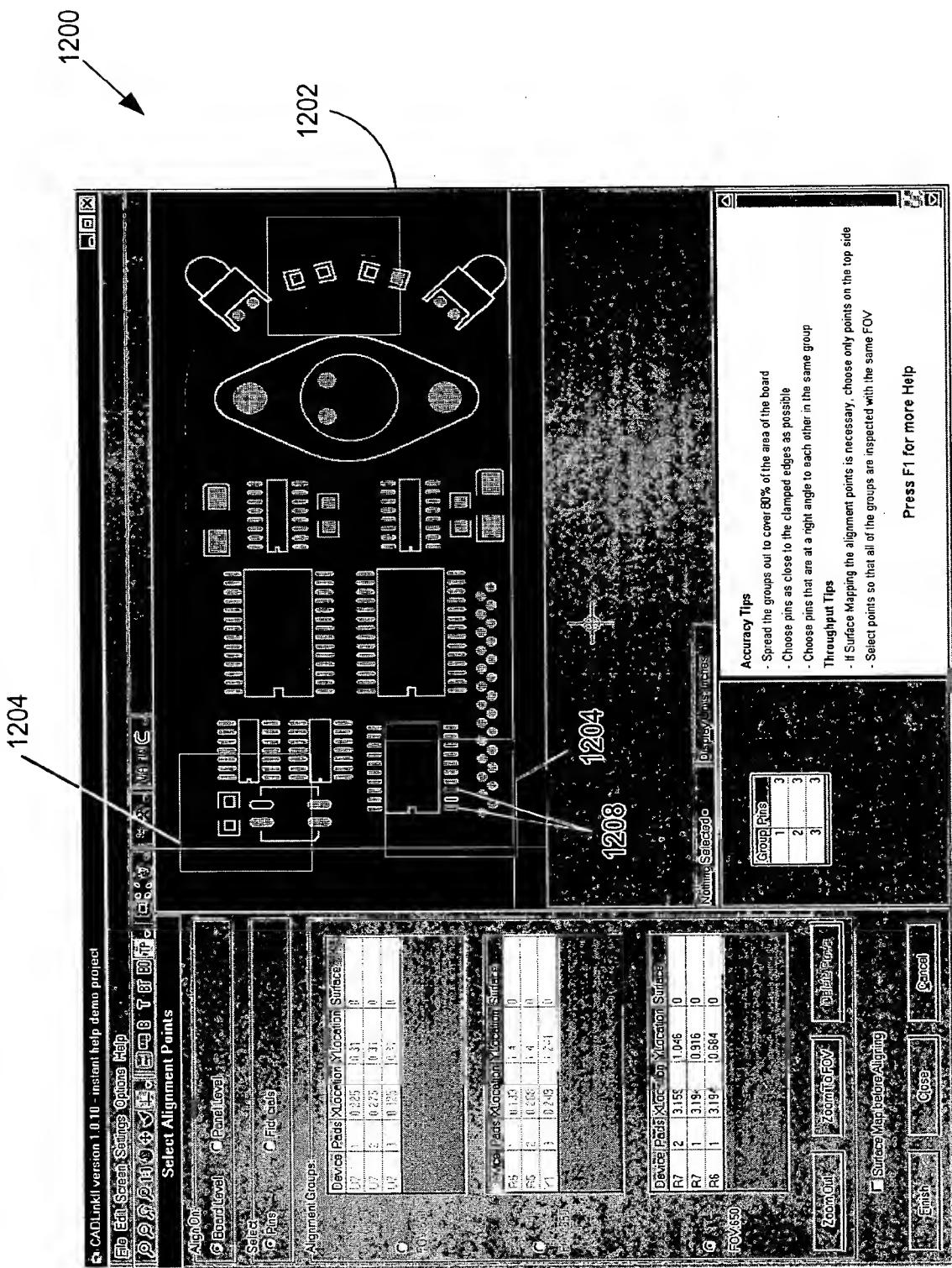
**FIG. 9**



**FIG. 10**



**FIG. 11**



**FIG. 12**